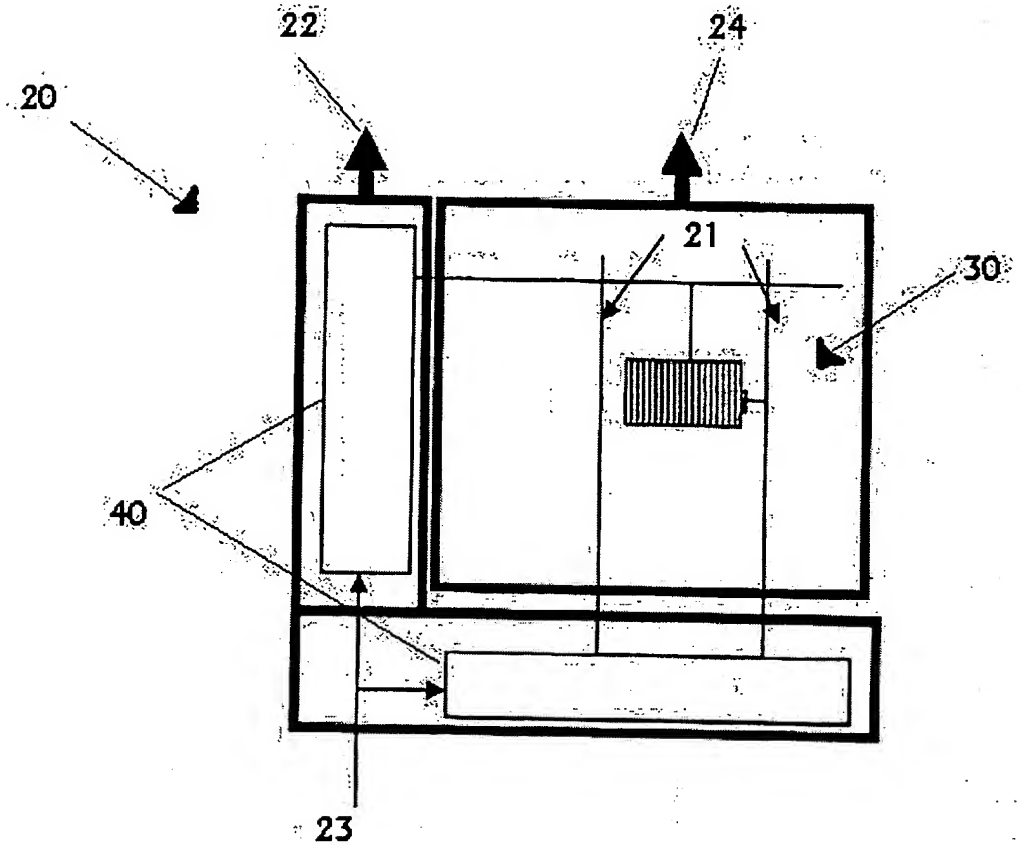

FIG. 1

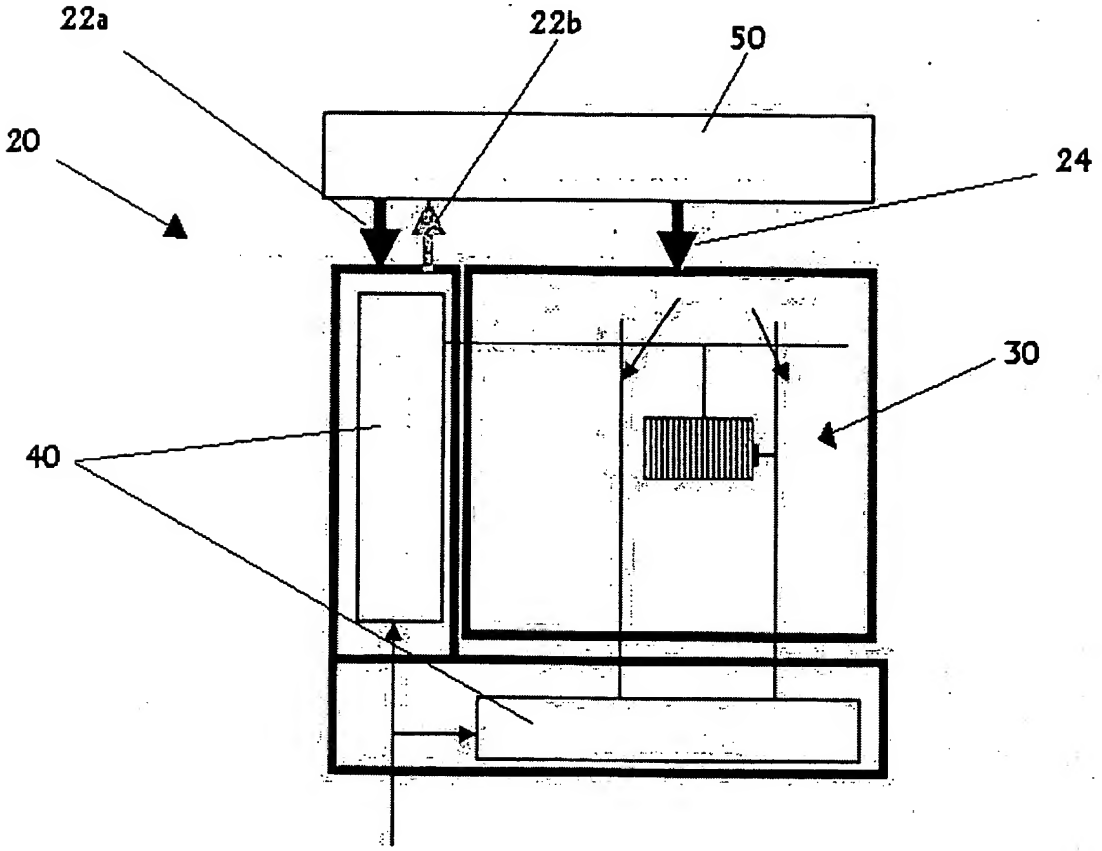

FIG. 2

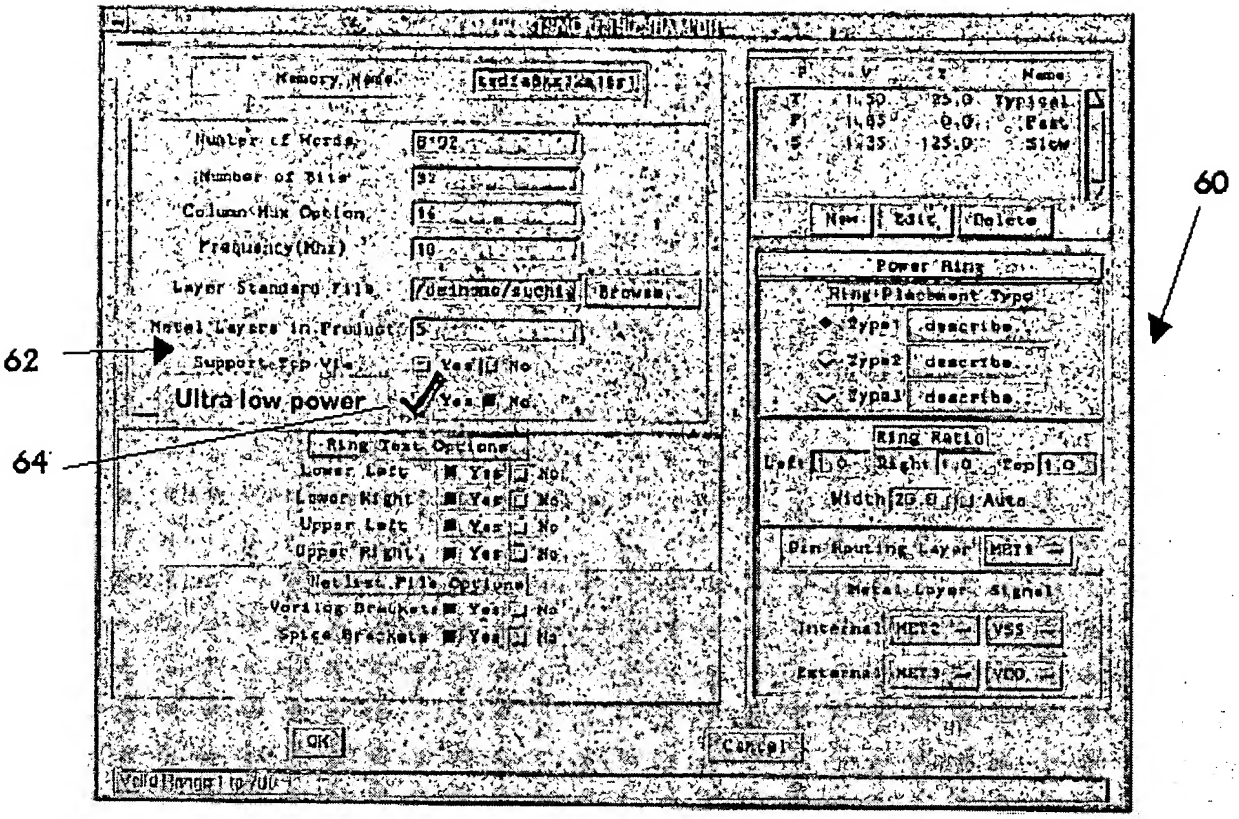

FIG. 3

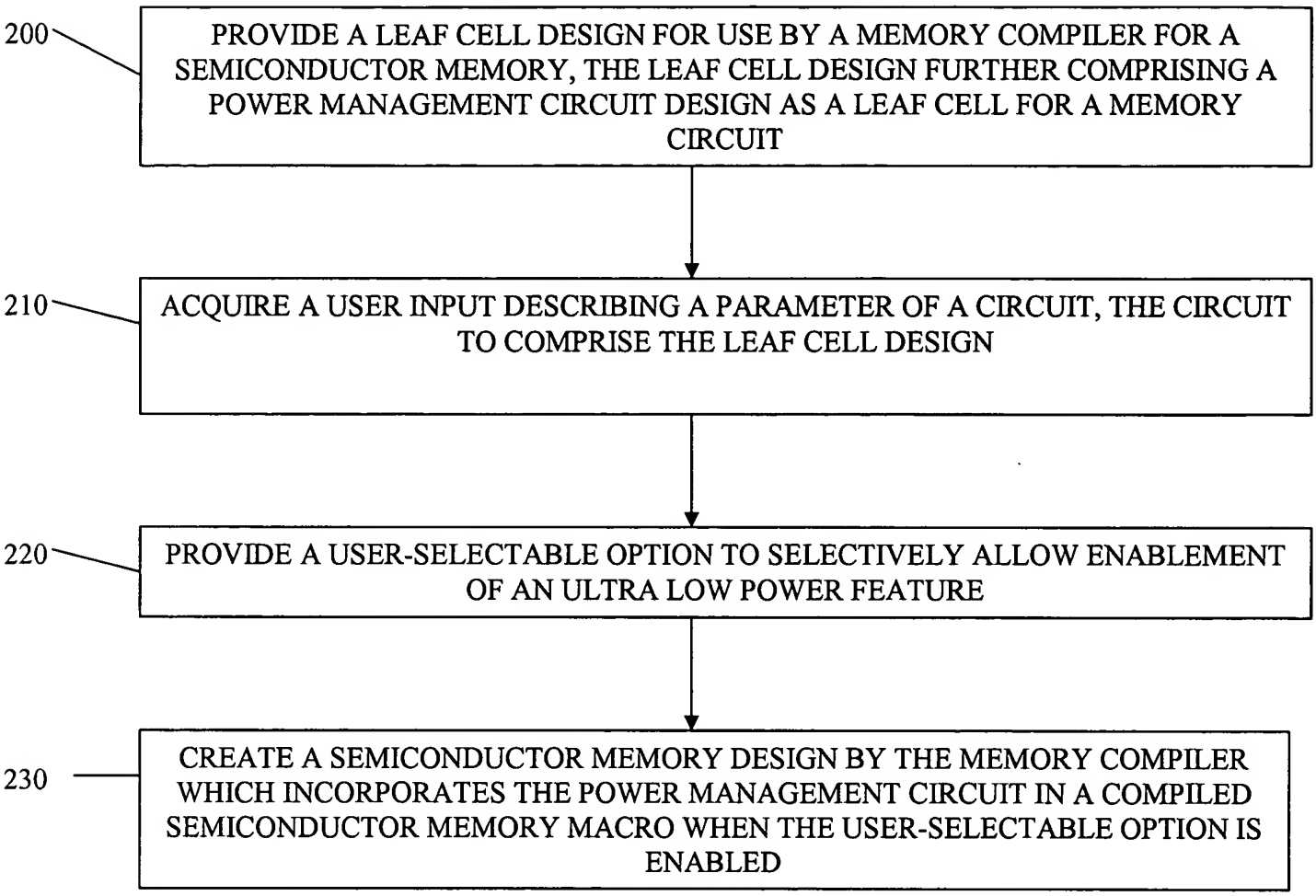
200
PROVIDE A LEAF CELL DESIGN FOR USE BY A MEMORY COMPILER FOR A SEMICONDUCTOR MEMORY, THE LEAF CELL DESIGN FURTHER COMPRISING A POWER MANAGEMENT CIRCUIT DESIGN AS A LEAF CELL FOR A MEMORY CIRCUIT
210
ACQUIRE A USER INPUT DESCRIBING A PARAMETER OF A CIRCUIT, THE CIRCUIT TO COMPRISE THE LEAF CELL DESIGN
220
PROVIDE A USER-SELECTABLE OPTION TO SELECTIVELY ALLOW ENABLEMENT OF AN ULTRA LOW POWER FEATURE
230
CREATE A SEMICONDUCTOR MEMORY DESIGN BY THE MEMORY COMPILER WHICH INCORPORATES THE POWER MANAGEMENT CIRCUIT IN A COMPILED SEMICONDUCTOR MEMORY MACRO WHEN THE USER-SELECTABLE OPTION IS ENABLED

FIG. 4